

I hereby certify that this correspondence is being electronically transmitted via EFS to the United States Patent and Trademark Office on the date shown below:

15 May 2008

Jeff Lloyd
Jeff Lloyd, Patent Attorney, Reg. No. 35,589

OK TO ENTER: /QJ/

06/19/2008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner : Quovaunda Jefferson
Art Unit : 2823
Applicant : Dong Yeal Keum
Serial No. : 10/747,601
Filed : December 29, 2003
Confirm. No. : 6479
For : Method for Fabricating a Transistor

Mail Stop **AF**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. §1.116

Sir:

In response to the Office Action dated March 17, 2008, Applicant submits the following remarks: